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		PATENTS AND PU			200208055-1 APPLICANT				
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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

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ATTY, DOCKET NO.	APPLICATION NO.	CONFIRMATION	NO.
200207937-1	10/606,714	TBA	
APPLICANT			
Naysen J. Robertson,	et al.		
FILING DATE	GROUP		

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REFERENCE DESIGNATION

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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

ATTY, DOCKET NO.	APPLICATION NO.	CONFIRMATION	NO.
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APPLICANT			
Naysen J. Robertson,	et al.		
FILING DATE	GROUP		
08-28-2003	TOA		

REFERENCE DESIGNATION

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Approved for use through 07/31/2006. OMB 0651-0031
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known				
Application Number	10/606,714	—		
Filing Date	June 26, 2003			
First Named Inventor	Naysen J. Robertson			
Art Unit	2113			
Examiner Name	BEAUSOLIEL, Robert W			
Attorney Docket Number	200207937-1			

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Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (F Anown)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		JP 2000172536	06-23-2000	NEC Corp		1
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Examiner Signature Many Catherine Brown	Date Considered	3/17/06

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U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE <u>ff 19</u>95. Under the Paperwork Reduction Act to a collection of information unless it contains a valid OMB control number Complete If Known Substitute for Form PTO-1449 AUS 1 7 2005 Applicant Number 10/606,714 INFORMATION Filing Date June 26, 2003 First Named Inventor Naysen J. Robertson STATEMENT BY Art Unit 2113 (Use as many sheets as necessary)
2 | Of **Examiner Name** BEAUSOLIEL, Robert W.

Attorney Docket

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Signature

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. "Failure logging system includes diagnostic controller which releases interruption to output interrupt signal to CPU, when	T²		
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